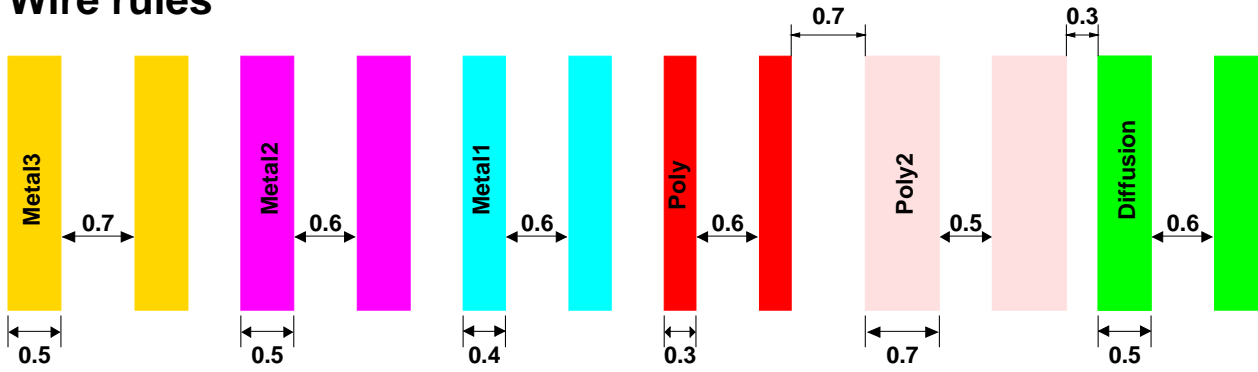


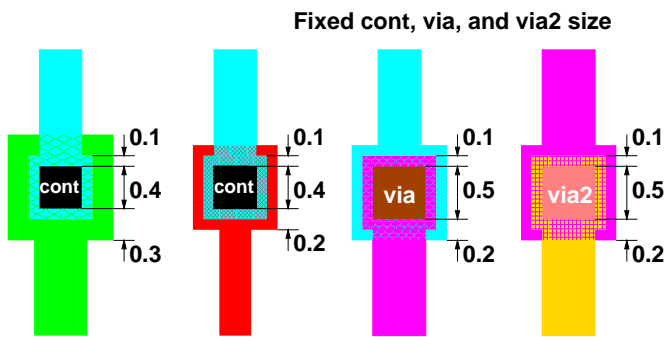
Geometrical design rules

All numbers are minimum dimensions in micron

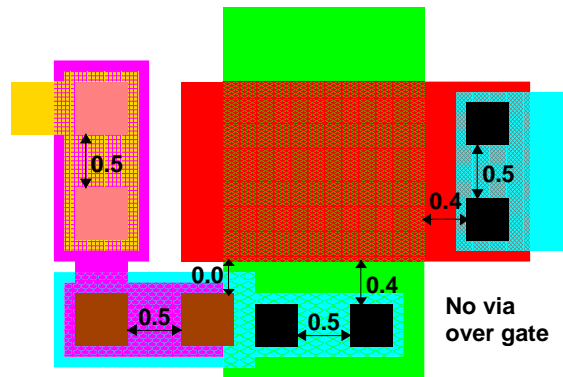
Wire rules



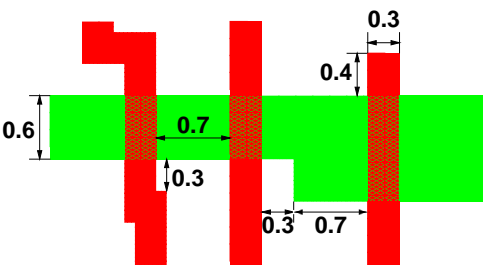
Contact rules



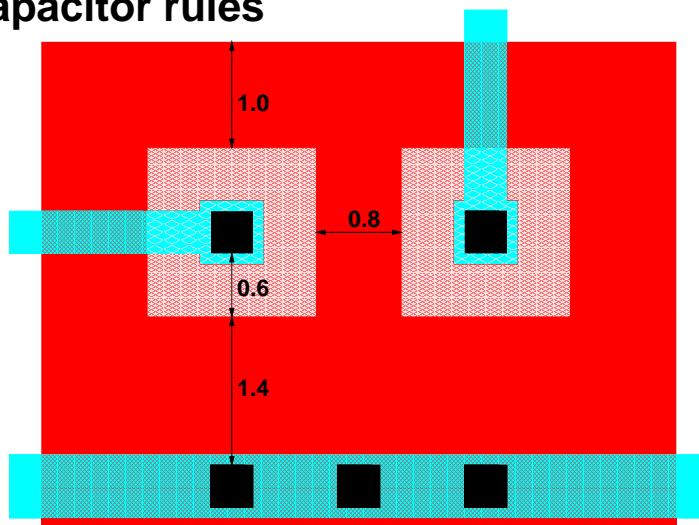
Contact and three-layer rules



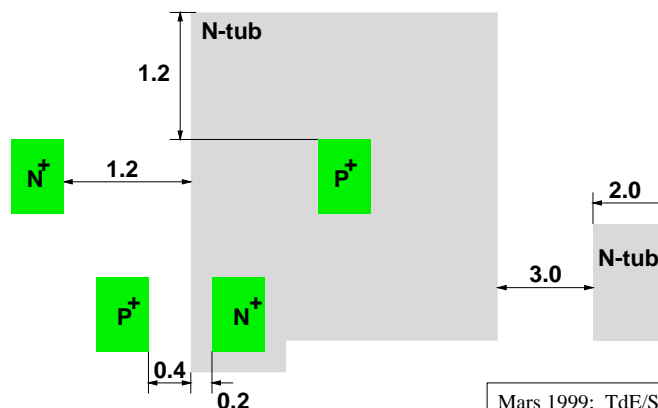
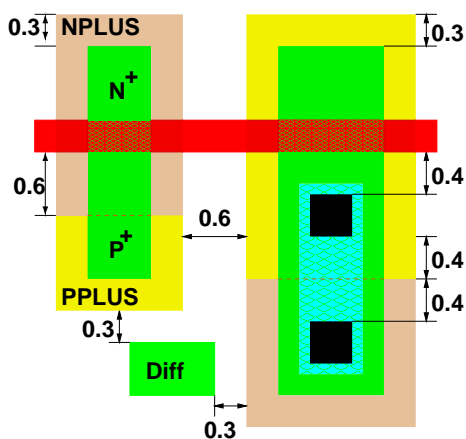
Transistor rules



Capacitor rules



Well and substrate rules



Maximum distance between two substrate contacts = 50