

4th. ed.

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LUND UNIVERSITY

Dept. of EIT

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ETI063 Analogue IC-design 2010 / 2011 Ht1 Schedule

Schedule for fall term 1 (HT1), 2010 (Preliminary):

week	day	date	time	room	activity	group	teacher	content
1 (36)	Mon.	10-08-30	13-15	E:C	L1		MT	<i>Introduction & MOS transistors</i> pages 1-8, 38-73
	Wed.	10-09-01	15-17	E:B	L2		MT	<i>MOS transistors continued</i>
	Thu.	10-09-02	08-10	E:0522	E1		AA	Problems 1.1, (1.2), 1.15, 1.16, 2.12, 2.17, (2.18), 12.19
2 (37)	Mon.	10-09-06	13-15	E:C	L3		MT	<i>Technology, Layout & Passive components</i> pages 78-88, 127-152
	Wed.	10-09-08	15-17	E:B	L4		MT	<i>Single transistor and multiple transistor amplifiers</i> pages 168-173, 178-182, 186-191, 195-197, 200-202, 206-213, (213-215), 218-231, (236-246).
	Thu.	10-09-09	08-10	E:0522	E2		AA	Problems 2.1, 2.2, 2.15 and old exam problems (see Course Material)
3 (38)	Mon.	10-09-13	13-15	E:C	L5		MT	<i>Current mirrors & active loads</i> pages 253-255, 257-260, 266-274, 277-299, (302-303), (306-317).
	Wed.	10-09-15	15-17	E:B	L6		MT	<i>Output stages & Frequency response</i> pages 356-362, 382-391, (391-398), 488-493, 496-503, 509-514, 515-517, 525-527, 532-533, 542-544, (624-644), (664-680)
	Thu.	10-09-16	08-10	E:0522	E3		AA	Problems 3.4, 3.7, 3.9, (3.14), 3.15, 3.24, (3.26), (3.27) 4.3, 4.5, 4.10 (one lref only), 4.14, (4.15), (4.17), (4.35)
4 (39)	Mon.	10-09-20	13-15	E:C	L7		MT	<i>CAD tools</i>
	Tue.	10-09-21	13-17	E:2424	Lab1	1	JL, MÄ	Measurement of MOS model parameters
	Wed.	10-09-22	08-12	E:2424	Lab1	2	JL, MÄ	Measurement of MOS model parameters
	Thu.	10-09-23	08-10	E:0522	E4		AA	Problems 7.2, 7.5, 7.18, (7.28), 7.29, 7.33, 7.38, (7.43), 7.48 Ignore ZTC and SCTC in all problems!
5 (40)	Mon.	10-09-27	13-15	E:C	L8		MT	<i>OP-amps and frequency compensation</i> pp. 404-411, 419-442, 442-450, (553-574), (579-593), (599-610), (612-615), 644-656, 680-685, (686-691)
	Tue.	10-09-28	13-17	E:2435	Lab2	1	JL, MÄ	Schematic drawing & simulation
	Wed.	10-09-29	08-12	E:2435	Lab2	2	JL, MÄ	Schematic drawing & simulation
	Thu.	10-09-30	08-10	E:0522	E5		AA	Problems 6.1, (6.2), 6.4, 6.5, (6.7), 6.10, 6.11, (6.12), (6.15), 6.18
6 (41)	Mon.	10-10-04	13-15	E:C	L9		MT	<i>Noise</i> pp. 748-757, 758-762, 765-768, 773-776, (776-779), 788-798, (799-802)

	Tue.	10-10-05	13-17	E:2435	Lab3	1	JL, MÅ	Simulation of an operational amplifier
	Wed.	10-10-06	08-12	E:2435	Lab3	2	JL, MÅ	Simulation of an operational amplifier
	Thu.	10-10-07	08-10	E:0522	E6		AA	Problems 9.1, 9.2, 9.5, (9.6), (9.8), 9.21, 9.25 (W/L9=100/8), (9.31), (9.37) 11.5, 11.8
7 (42)	Mon.	10-10-11	13-15	E:C	L10		MT	<i>Differential OP-amps</i> pp. 808-823, (823-832), 835-845, (845-857)
	Tue.	10-10-12	13-17	E:2435	Lab4	1	JL, MÅ	Layout of a two-stage amplifier
	Wed.	10-10-13	08-12	E:2435	Lab4	2	JL, MÅ	Layout of a two-stage amplifier
	Thu.	10-10-14	08-10	E:0522	E7		AA	Problems 11.25, 11.28, 12.1, (12.9), (12.10), 12.16, (12.20)

MT = Markus Törmänen

AA = Andreas Axholt

JL = Jonas Lindstrand

MÅ = Mats Ärlelid

L = Lecture, E = Exercise, Lab = Laboratory

Exam: Tuesday Oct. 19th, 14pm-19pm, room MA:10D,10E.

Comments:

Lectures:

Generally, only CMOS parts in the book are taken into account.

Pages in brackets are supplementary reading and may help you get a better understanding or refresh previous knowledge.

Exercises:

The course data sheet is to be used for all problems, not the data given in chapter 2 in the book.

If MOSFET voltage ranges are given in the problem, please scale these ranges to the technology we use. For example, a supply voltage of 3V is not allowed in our technology, and this should thus be scaled to 1.2V instead.

SPICE simulations are left as an exercise to the student. Problems in brackets () are useful to do but not to be prioritized.

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Dept. of Electrical and information technology, LTH, Box 118, 221 00 Lund. Telefon: 046-222 00 00