





Example: HTTP server	A THE
server client	v t
	RSA







Implementation Methods: Foreground/Background
The background task is similar to the main loop in a polled system.
The foreground task is interrupt service routines (ISRs) that handle critical events.

















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# Inter-task Communication: Monitors

#### Monitors

- Protects an object by mutual exclusion (mutex) and also coordinates multiple tasks trying to access the same object.
- Tasks that try to enter a monitor when it's locked by another task is put in a waiting queue and will be started when the monitor is free.

#### AKEA

























# Dijkstra Semaphore

The example implementation uses busy wait when a task waits for a semaphore. In reality, a semaphore is never implemented like that. Tasks that are waiting for a semaphore are placed in a waiting queue and started one at a time for each signal().

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# Example: The Mars Rover Pathfinder

- Landed July 4th 1997
- Everything was perfect!
- Everything?
- After a few days......the system began to
- ...the system began to reset sporadically
   L. Sha, R. Rajkumar, and J. P. Lehoczky. Priority Inheritance Protocols: An Approach to Real-Time Synchronization. In IEEE Transactions on Computers, vol. 39, pp. 1175-1185, Sep. 1990.



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# Deadlock Simplified, deadlock occurs when a set of tasks wait on each others resources to become free. There are algorithms both to avoid deadlocks and to detect and resolve deadlocks. However, often deadlocks are avoided by a design and debug process which can't guarantee deadlock-free systems.



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# Scheduling methods

#### Deadline driven scheduling (DDS)

- When the system also has aperiodic tasks, deadline driven scheduling can be used.
- DDS: schedule the task with the earliest deadline first.

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#### Performance aspects

- Throughput, both in number of events/s and bytes/s is often an important performance aspect.
- Latency/response time is often also important.
- The *critical path* concept from hardware design can be a useful way of analyzing a system.

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# Performance Aspects: Context Switching

- Context switches is often a fairly expensive operation, involving saving/restoring all processor registers etc.
- It's important to use appropriate task communication methods that don't introduce unnecessary context switches, e.g., don't use message passing for accessing a shared database.

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# Performance Aspects: Critical Regions

- Since mutex is implemented by disabling interrupts, the amount of time when interrupts are disabled, influences how fast an event (e.g., timer or packet interrupt) can be handled.
- It's therefore important to minimize these regions.

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### Memory Management

- A large part of the memory allocation code must be performed with mutex. Performance therefore makes it important to minimize memory allocation calls.
- One solution is to allocate memory in advance and manage it in block-pools within each sub-system. This reduces allocation calls and also makes it easier to predict memory requirements.

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# **Memory Management**

- Memory leaks is a very common problem in complex system. This usually stems from that it's unclear who is responsible for deallocating memory of data that is passed between subsystems.
- Automatic methods for deallocation memory is therefore interesting.

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# Garbage Collect

 Special real-time garbage collection algorithms have been developed but this is still a somewhat immature area and consequently not used in many systems.

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# Debugging Real-time Systems

- One non-intrusive method is to use a logic analyzer to trace what is happening in the system. A logic analyzer with large trace depth and good triggering functions can be as useful as a source-level debugger.
- If the processor has an internal cache, the usefulness of a logic analyzer is reduced since much of what's going on inside is hidden.

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<ul> <li>There are many other tricks of the trade <ul> <li>Prevent and find errors earlier!</li> <li>Design for quality</li> <li>Test-driven development</li> <li>Reviews</li> <li>Static analysis</li> <li>Software instrumentation</li> </ul> </li> <li>Post mortem debugging. Trace a small part of what's happening in the system, into memory. After an error has occurred, the trace can be printed.</li> <li>Write trace data (e.g., one byte task-id, state) to an i/o port that can easily be traced with a cheap logic analyzer.</li> <li>Use a memory emulator, that lets you inspect the memory of the running system without significant disturbance.</li> <li>JTAG</li> </ul>

Special considerations for embedded	-1/2
Evention bondling	
P Exception handling	
Graceful degradation	
Self-testing and watchdog	
	<u>axsa</u>