



Digitaltechnik EITF65

Lecture 2: Finite State Machines

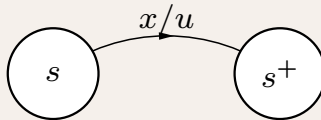
State transition graphs

Definition

A **state transition graph** is a graph where

- ▶ **the state** is an abstract (and compact) description of the past. It contains all relevant information about the history.
- ▶ in each state there is **one branch for each input**.
- ▶ in each state there is **one output combination for each input**.

The branches between the states are drawn as arrows labeled with the input and the output.



Definition (2.2)

A **state machine** is defined by the five-tuple $\mathcal{M} = \{\mathcal{I}, \mathcal{S}, \mathcal{Z}, \delta, \lambda\}$ where

\mathcal{I} : is the input set.

\mathcal{S} : is the state set.

\mathcal{Z} : is the output set.

$\delta: \mathcal{S} \times \mathcal{I} \rightarrow \mathcal{S}$ is the *state transition* function.

$\lambda: \mathcal{S} \times \mathcal{I} \rightarrow \mathcal{Z}$ is the *output* function.

The behavior of a state machine can be described by a **state transition graph**.

Definition

A **trellis** is a (directed) graph where

- ▶ each node represents a state at a certain depth.
- ▶ there is a set of *root vertices* that does not have any entering edges.

The depth of a root vertex is zero. The depth of other vertices are defined as the number of edges from a root vertex.

Parity check

Example 2.2

Check if a sequence has even or odd parity, i.e., if the number of ones is even or odd.



where

$$z_k = \begin{cases} \text{even} & \text{if } \sum_{\ell=0}^k i_{\ell} \text{ even} \\ \text{odd} & \text{if } \sum_{\ell=0}^k i_{\ell} \text{ odd} \end{cases}$$

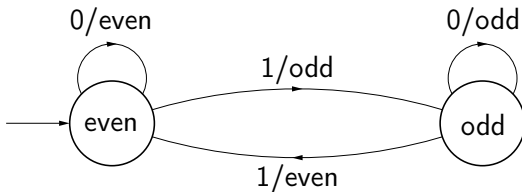
\mathcal{I} : = $\{0,1\}$ is the input set,

\mathcal{Z} : = $\{\text{even}, \text{odd}\}$ is the output set

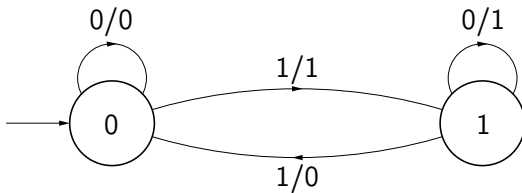
\mathcal{S} : = $\{\sum_{\ell=0}^{k-1} i_{\ell} \text{ even}, \sum_{\ell=0}^{k-1} i_{\ell} \text{ odd}\}$

We can use a state transition graph to define δ and λ .

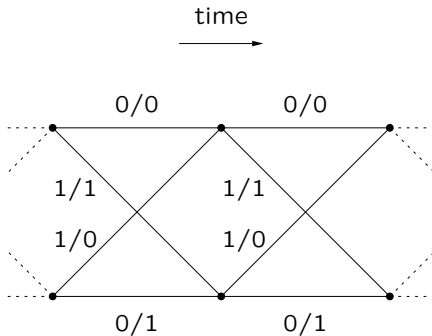
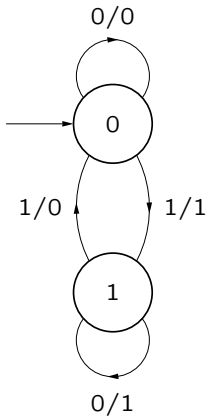
Parity check (Graph)



Let even = 0 and odd = 1 to get:



Parity check (Trellis)



Example 2.3

Give 1 as output if and only if the last four input bits matches 0011, otherwise 0.

E.g.,

$$\begin{array}{rccccccc} i: & \dots & 0011 & \dots & 0011 & \dots & \\ & & \downarrow & & \downarrow & & \\ z: & \dots & 00010 & \dots & 00010 & \dots & \end{array}$$

Question: What are

\mathcal{I} , (input set)

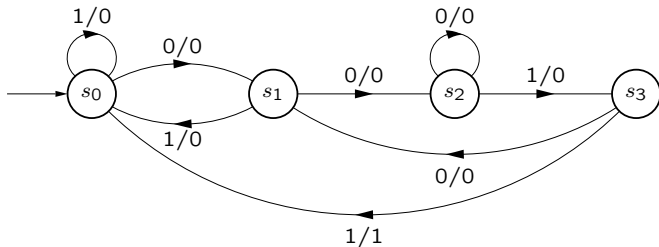
\mathcal{S} , (state set)

\mathcal{Z} , (output set)

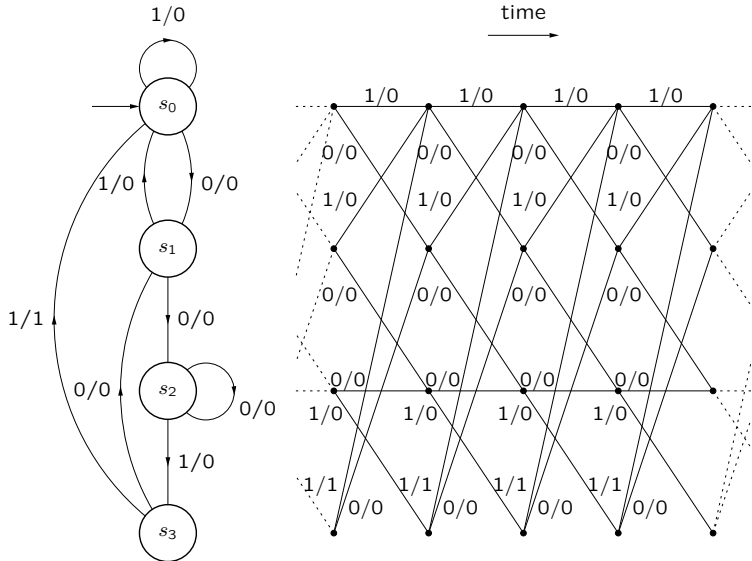
δ , $\mathcal{S} \times \mathcal{I} \rightarrow \mathcal{S}$ (*state transition function*)

λ , $\mathcal{S} \times \mathcal{I} \rightarrow \mathcal{Z}$ (*output function*)

Detector (Graph)



Detector (Trellis)



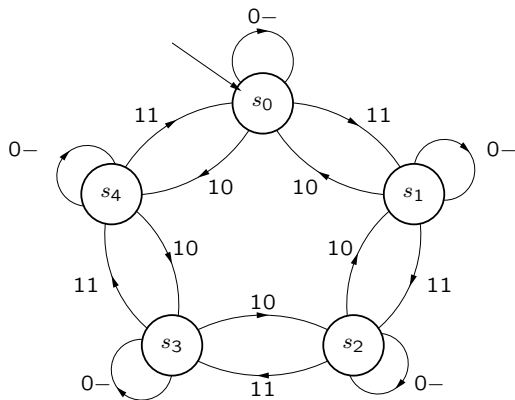
Modulo 5 counter

Example

Construct a system that works as a modulo 5 counter. It has two inputs, Count and U/D. If Count is 0 it does not count and if it is 1 it counts. If U/D is 1 it adds one to the result and if 0 it subtracts one. (There is no explicit output here.)

Count	U/D	state	next state
0	0	Q	Q
0	1	Q	Q
1	0	Q	$Q-1 \bmod 5$
1	1	Q	$Q+1 \bmod 5$

Modulo 5 counter (Graph)



The lion threat (two lions)

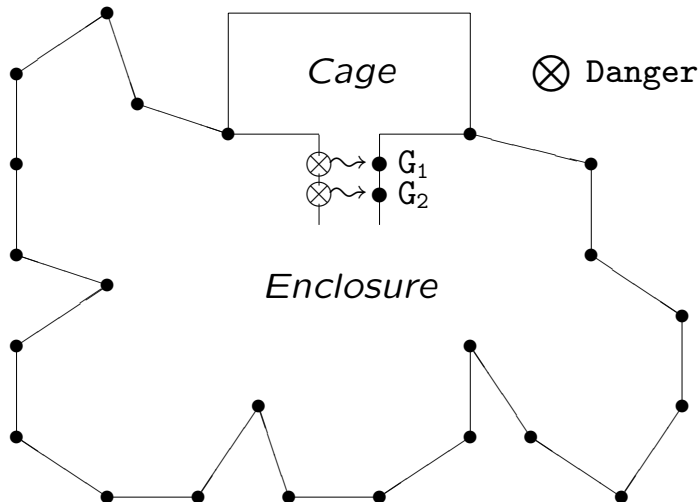
Example 2.4

In a zoo there is an enclosure for lions. It consists of two parts, a cage where the lions can rest and the enclosure, which is a big open field where the lions can stroll around. The enclosure is hilly and, therefore, difficult to overview. Since the keeper wants to clean the enclosure while the lions are in the cage he needs a system where a lamp, Danger, tells if there is any lion in the enclosure.

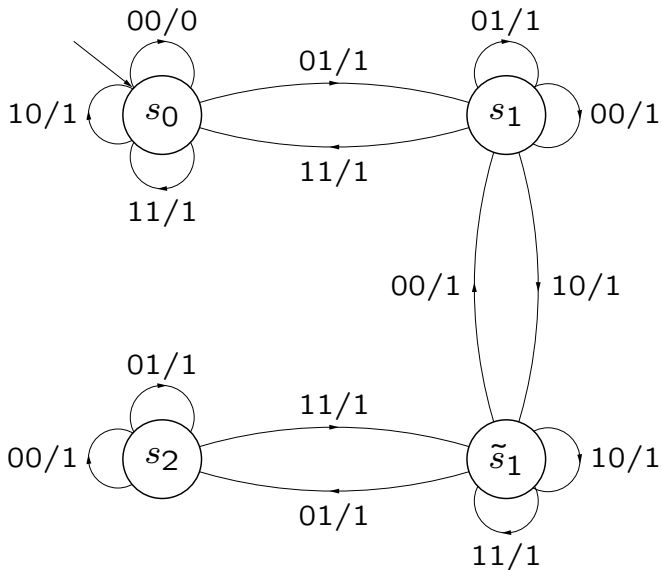
At the passage between the cage and the enclosure there are two photo detectors, G_1 and G_2 that are parted by half a meter. The lions are about two meters long and cannot pass the passage at the same time. They cannot stop or turn in the passage either.

Construct a graph that solves the problem for two lions.

The lion threat (figure)



The lion threat (Graph)

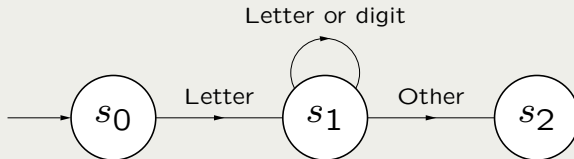


Variable name

Example 2.8

In programming languages the variable names often consist of a letter followed by letters or digits.

The name ends when the sequence of letters and digits ends, i.e., when some other character is written.



Event controlled or clock controlled

When do we change state, i.e., follow an edge?

- ▶ **New event** can cause a state transition. On each new input, look at input and state, and make a state transition. *Example:* Variable name.
- ▶ **External clock signal** can cause state transition. On each clock signal, look at input and state, and make a state transition. *Example:* Modulo 5 counter.

Clock controlled situations often require additional states since the clock can be very fast!

- ▶ Modulo 5 counter would require extra states
- ▶ Lion threat would not require extra states

Design flow

