



Digitaltechnik EITF65

Martin Hell

Lecture 1: Introduktion

Kursen Digitalteknik

Undervisningsmoment

Föreläsning:	$14(LP1) + 5(LP2) = 19$
Övning:	3 grupper (E, D, mix)
Laborationer:	$3 + 3 = 6$, våning 4 i E-huset
Poäng:	9 hp

Personer

Föreläsare:	Martin Hell (F1-F14) (martin.hell@eit.lth.se)
Föreläsare:	Andreas Johansson (F15-F19) (andreas.johansson@eit.lth.se)
Övningsassistent:	Erik Mårtensson, Linus Karlsson
Labbansvarig:	Christoffer Cederberg
Kursadministratör:	Anne Andersson

Mer formalia

Web:

www.eit.lth.se/course/eitf65

Litteratur:

- Design of Digital Circuits
Rolf Johannesson and Ben Smeets
- Laborationer i Digitalteknik (säljs på KFS för 360:-)
- Online: Solutions to Selected Problems
(Digitala kretsar, (L-H. Hemert), är ett bra komplement)

Tentamen: A set of problems

0-19: U

20-29: 3

30-39: 4

40-50: 5

Veckoschema ht1

Vecka 1 F1, F2, Ö, F3, Ö

Vecka 2 F4, F5, Ö, F6, Ö

Vecka 3 F7, Ö, F8, Ö

Vecka 4 F9, Ö, F10, Ö

Vecka 5 F11, Ö, F12, Ö

Vecka 6 F13, Ö, Ö

Vecka 7 F14, Ö, Ö

Labb 1 08.15-12 eller 13.15-17 i vecka 3 anmälan via webb

Labb 2 08.15-12 eller 13.15-17 i vecka 5 anmälan via webb

Labb 3 08.15-12 eller 13.15-17 i vecka 7 anmälan via webb

Förändringar i kursen

- ▶ Ny föreläsare och kursansvarig
- ▶ Tentan placerad efter LP1 istället för efter LP2
- ▶ 14 F i LP1 + 3 F i LP2 har nu "klämts" in i 14 F i LP1 och är grund för tentan.
 - ▶ Tidigare kommentarer om att det gått långsamt
 - ▶ Numera mindre fokus på bevis på föreläsningarna
- ▶ 3 F i LP2 har expanderats till 5 F i LP2. VHDL mer "från början".
 - ▶ Tidigare kommentarer om att det funnits lite introduktion till VHDL
- ▶ 3 + 3 labbar istället för 2 + 4.

Mål

Mer logisk uppdelning, mindre glapp mellan teori och tenta, bättre introduktion till VHDL.

Plan för föreläsningar

Lecture	Part	Topic
Lectures 1-3	Chapter 2	Introduction to digital systems, finite state machines, and sequential circuits
Lectures 4-6	Chapter 3-4	Discrete Mathematics (Rings and Boolean algebra), arithmetics
Lectures 7-8	Chapter 5	Boolean functions (normal forms, minimizing,)
Lectures 9-11	Chapter 6	State minimisation, assignment, and asynchronous sequential circuits
Lecture 12	Section 5.4	Standard components
Lectures 13-14	Chapter 7	Linear sequential circuits (Time domain and D -domain), LFSRs
Lectures 15-19	notes will be distributed	Modern design of digital systems (transistor-level, FPGA development tools, the VHDL language)

Plan för labbar

Labb	Part	Topic
Labb 1	Chapter 2	Lejonburen
Labb 2	C. 3-5	ALU
Labb 3	C. 4-6	Latchar och Vippor
Labb 4	VHDL	Stop Watch
Labb 5-6	VHDL	Microcontroller unit

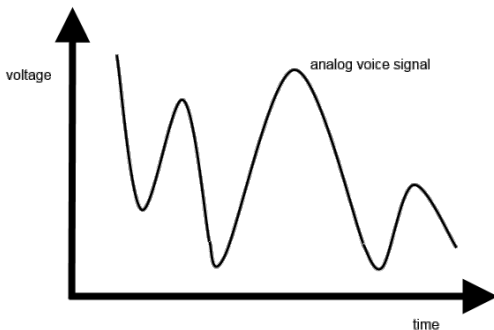
Note: You must understand the lab in order to pass!

Introduction to Digital Design

- ▶ What is a digital system?
- ▶ How do we best construct digital systems?
- ▶ How can theory and a systematic approach aid us in the design process?

Signals

A *signal* is a function that conveys information about the behavior or attributes of some phenomenon. In the physical world, any quantity exhibiting variation in time or variation in space (such as an image) is potentially a signal that might provide information on the status of a physical system, or convey a message between observers, among other possibilities. (Wikipedia)



Digital vs Analog

- ▶ A digital signal is a signal that takes discrete values.
- ▶ An analog signal is any continuous signal.

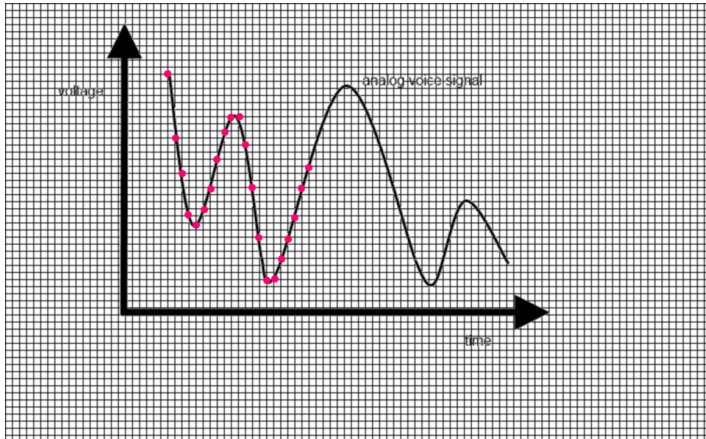
Usually, a digital signal takes discrete values in discrete time (*synchronous signals*).

We may consider time t as $t \in \mathbb{Z}$.

If time is continuous, it is called an *asynchronous* signal.

Conversions

- ▶ Analog signals can be converted to digital form (AD-converters)
- ▶ Digital signals can be converted back to analog form (DA-converters)



Example

Music on a CD

- ▶ is sampled at 44100 Hz
- ▶ uses 16 bits (2 bytes) to describe each sample
- ▶ should last for up to 74 minutes
- ▶ should have 2 different channels

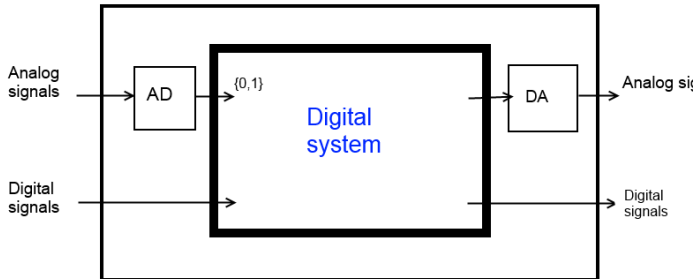
$$44100 \cdot 2 \cdot (74 \cdot 60) \cdot 2 \approx 740\text{MB of data}$$

To think about: Why can a CD-ROM only hold 650MB of data?

The smallest alphabet size for digital signals is the binary alphabet $\{0, 1\}$.

- ▶ Digital input and digital output signals.
- ▶ Discrete time $t \in \mathbb{Z}$.
- ▶ The digital system processes/operates on input and delivers output. All in binary form.

Digital systems



All things we build today are digital!

Example in communication: mobile communication systems:

- ▶ NMT (1G) analog system (1981-)
- ▶ GSM (2G) digital system (1991-)
- ▶ UMTS (3G) digital system (2001-)
- ▶ LTE (4G) digital system (\sim 2009-)
- ▶ 5G digital system (2020(?)-)

Why digital?

- ▶ Bandlimited analog signals can be (almost) exactly represented in digital form (Nyquist-Shannon sampling theorem).

Theorem

If a function $f(t)$ contains no frequencies higher than B hertz, it is completely determined by giving its ordinates at a series of points spaced $1/(2B)$ seconds apart.

Thus, we need to sample at $2B$ samples per second (or more).

Return to CD example

- ▶ We sample at 44100 Hz
- ▶ Human hearing range is roughly 20 - 20,000 Hz \Rightarrow we need a sample rate of at least 40000 Hz. (Filter attenuation require us to have it slightly higher.)

Why digital?

Bit stream representing the signal gives us nice features

- ▶ Processing/Communication without errors. See course *EITN70 Channel Coding for Reliable Communication*.
- ▶ Compress data. See course *EITN45 Information Theory*.
- ▶ Encrypt data. See course *EDIN01 Cryptography*.
- ▶ Operate on and manipulate digital signals. See course *EITA50/EITF75 Digital Signal Processing*.
- ▶ Digital electronics are cheap. (Modern processor has around 2 billion transistors and costs around \$100). See e.g., *ETIN20 Digital IC design* and *EITF35 Introduction to Structured VLSI Design*

So, we need to learn how to operate on discrete values.

This course: Build circuits that take (a sequence of) discrete values as input and output (a sequence of) discrete values.

Why analog then?

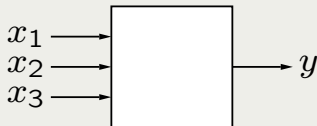
- ▶ The world is analogue. 0 and 1 is made up.
- ▶ Air pressure, voltage, current, polarization of magnets, etc is continuous.
- ▶ Digital circuits are constructed using analogue circuits.
- ▶ Digital transmission is realized using analogue signals.

Majority Function

Example

Let $y(x_1, x_2, x_3)$ be the majority function,

$$y(x_1, x_2, x_3) = \begin{cases} 0, & x_1 + x_2 + x_3 \in \{0, 1\} \\ 1, & x_1 + x_2 + x_3 \in \{2, 3\} \end{cases}$$



x_1	x_2	x_3	y
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

The lion threat

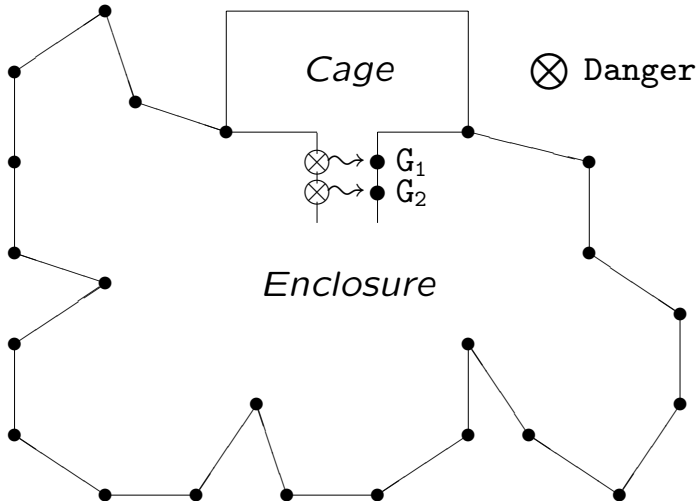
Example (Lab 1)

At a zoo there is an enclosure for lions. It consists of two parts, a cage where the lions can rest and the enclosure, which is a big open field where the lions can stroll around. The enclosure is hilly and, therefore, difficult to overview. Since the keeper wants to clean the enclosure while the lions are in the cage he needs a system where a lamp, Danger, tells if there is any lion in the enclosure.

At the passage between the cage and the enclosure there are two photo detectors, G_1 and G_2 that are parted by half a meter. The lions are about two meters long and cannot pass the passage at the same time. Neither can they stop or turn in the passage.

Construct a circuit that solves the problem for one lion.

The lion threat (figure)



The lion threat

Example (Sequences)

- ▶ The lion walks from the cage to the enclosure:

$$(G_1, G_2) : 00 \rightarrow 10 \rightarrow 11 \rightarrow 01 \rightarrow 00$$

- ▶ The lion walks from the enclosure to the cage:

$$(G_1, G_2) : 00 \rightarrow 01 \rightarrow 11 \rightarrow 10 \rightarrow 00$$

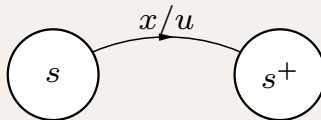
State transition graphs

Definition

A **state transition graph** is a graph where

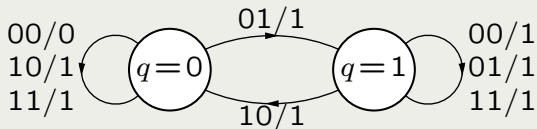
- ▶ **the state** is an abstract (and compact) description of the past. It contains all relevant information about the history.
- ▶ in each state there is **one branch for each input**.
- ▶ in each state there is **one output combination for each input**.

The branches between the states are drawn as arrows labeled with the input and the output.



The lion threat

Example (Solution)



$q G_1 G_2$	q^+	DANGER
0 0 0	0	0
0 0 1	1	1
0 1 0	0	1
0 1 1	0	1
1 0 0	1	1
1 0 1	1	1
1 1 0	0	1
1 1 1	1	1

